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Use KCL to obtain currents  $i_1$ ,  $i_2$ , and  $i_3$  in the circuit shown in Fig. 2.72 given that  $i_a = 2$  amps,  $i_b = 3$  amps, and  $i_c = 4$  amps. Solution At node a,  $i_a + i_2 + i_b = 0$  or  $i_2 = -2 - 3 = -5$  amps At node b,  $i_b + i_1 + i_c = 0$  or  $i_1 = -3 - 4 - 7 = -14$  amps At node c,  $i_2 + i_3 + i_1 = 0$  or  $i_3 = -i_2 - i_1 = 5 + 14 = 19$  amps

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While constructing the circuit, rotate L1 counterclockwise through 270° so that current i(t) enters pin 1 of L1 and set IC = 10 for L1. After saving the schematic, select Analysis/Setup/Transient to change the Final Time to 1 s. Set the Print Step slightly greater than 0 (20 ns is default). The circuit is simulated by selecting Analysis/ Simulate.

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